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**DHANALAKSHMI SRINIVASAN COLLEGE
OF ARTS & SCIENCE FOR WOMEN
(AUTONOMOUS)**
(For Candidates admitted from 2020-2021 onwards)
PG DEGREE EXAMINATIONS APRIL - 2021



**M.Sc., - PHYSICS
ELECTRONICS**

Time: 3 Hrs

Max.Marks: 75

PART - A

CHOOSE THE CORRECT ANSWER

(10X1=10)

1. A JFET has three terminals, namely.....
 - a) Cathode, anode, grid
 - b) emitter, base, collector
 - c) Source, gate, drain
 - d) none of the above
2. Tunnel diode exhibits special characteristics known as.....
 - a) Positive reactance
 - b) positive resistance
 - c) Negative reactance
 - d) negative resistance
3. In the common mode
 - a) Both inputs are grounded
 - b) the outputs are connected together
 - c) an identical signal appears on both the inputs
 - d) the output signal are in-phase
4. The Op-amp can amplify
 - a) a.c. signals only
 - b) d.c. signals only
 - c) Both a.c. and d.c. signals
 - d) neither d.c. nor a.c. signals
5. In S-R flip-flop, if $Q = 0$ the output is said to be.....
 - a) Set
 - b) Reset
 - c) Previous state
 - d) Current state
6. A register that is used to store binary information is called.....
 - a) Data register
 - b) Binary register
 - c) Shift register
 - d) D – Register
7. The practical use of binary-weighted digital-to-analog converters is limited to:
 - a) R/2R ladder D/A converters
 - b) 4-bit D/A converters
 - c) 8-bit D/A converters
 - d) op-amp comparators

